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PAT-NO:

JP360133758A

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TITLE:

MANUFACTURE OF MOS TYPE SEMICONDUCTOR DEVICE

PUBN-DATE:

July 16, 1985

used for daims 1, 11,12,13

only obstract.

INVENTOR - INFORMATION:

NAME

COUNTRY

IWAMATSU, SEIICHI

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**APPL-NO:** JP58241442

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INT-CL (IPC): H01L029/78 , H01L027/12 , H01L029/50

US-CL-CURRENT: 257/E29.255 , 438/270

## **ABSTRACT:**

PURPOSE: To reduce the cost by a method wherein lead-out electrodes from the source and drain of an MOS type TFT formed on a glass substrate are created by the printing method.

CONSTITUTION: A semiconductor film 2 is formed on the surface of the glass substrate 1. First, resist is formed into a figure form by printing, and next the resist is removed by etching with the mixed solution of HF and HNO3. Then, an Si3N4 film 3 is formed by the plasma CVD method. Through holes for leading out the source and drain electrodes are formed in the film 3. The source, gate, and drain electrodes 4, 5, and 6 are formed by printing an ink of the mixture of W or Mo powder in a binder such as glycerin by the printing method such as screen printing, and then by solidification.

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